REMARKS

The Office Action dated April 24, 2007, has been received and carefully noted. The above amendments and the following remarks are being submitted as a full and complete response thereto. Claims 1-19 are pending in this application. By this Amendment, claims 1 and 17 are amended. Support for the subject matter of the amendment to claims 1 and 17 can be found in the Specification at, for example, page 8, lines 1-3. No new matter has been added. Reconsideration of the application is respectfully requested.

The Office Action rejects claims 1-3, 5 and 10-18 under 35 U.S.C. § 103(a) over Beyers et al. (U.S. Patent No. 5,534,942) in view of Knox et al. (U.S. Patent No. 6,351,292); claims 4 and 19 under 35 U.S.C. § 103(a) as being obvious over Beyers in view of Knox and Shibamiya et al. (U.S. Patent No. 5,926,174); claims 6-7 and 9 under 35 U.S.C. §103(a) as being obvious over Beyers, Knox and Suga et al. (U.S. Patent No. 6,215,467); and claim 8 under 35 U.S.C. §103(a) over Beyers and Knox in view of Shibamiya and Suga. It is noted that claims 1 and 17 have been amended. To the extent that the rejections remain applicable to the claims currently pending, the Applicant hereby traverses the rejections, as follows. .

The current application claims an image processing apparatus, and associated method, that includes an image memory, a display buffer memory and a control section for controlling access in the image memory and the display buffer memory, for reading a first image data group and writing the first image data group in the display buffer memory <u>without intervening with any other memory area or buffer memory</u>, wherein the control section includes a data expansion control section capable of selectively

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increasing a data amount of only the second image data group and of <u>outputting the data amount to the display buffer memory</u>, as recited in independent claim 1 and similarly recited in independent claim 17, as amended.

Beyers teaches a digital video signal processing system that receives encoded packets of data representing video image information in compressed form (Abstract). The Office Action admits that Beyers fails to explicitly teach writing the first image data group in a display buffer (Office Action, page 3, lines 7-8). The Office Action asserts, however, that because Beyers teaches a superimpose mode of operation in which a graphic image is inserted within a video image, Beyers *implicitly* teaches a display buffer, as recited in the independent claims. The Applicant respectfully disagrees. Even assuming, *arguendo*, that Beyers does "implicitly" teach a display buffer in which to temporarily store the first image and the second image (not admitted), there is no indication in Beyers of any reason or motivation at least as to how the first image data group would be written in the display buffer memory <u>without intervening with any other memory area or buffer memory</u>, as recited in independent claims 1 and 17, as amended. Knox <u>fails</u> to cure this deficiency in Beyers.

The Office Action also admits that Beyers fails to disclose or suggest the claimed control section. See *Office Action*, page 3, lines 14-16. Knox is cited as allegedly curing this additional deficiency of Beyers. However, Knox teaches in a "Line Doubling Mode," that an OSD unit would repeat each OSD line on the next line of video for an OSD region (see Knox, Abstract). Knox does not disclose or suggest a control that includes at least the feature of a data expansion control section capable of selectively increasing a data amount of only the second image data group read from said image

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memory, according to the second image data group, and of outputting the data amount to the display buffer memory, as recited in independent claims 1 and 17, as amended.

Accordingly, the combination of Beyers and Knox fails to disclose or suggest at least such a feature.

Shibamiya teaches in an apparatus for displaying the image of an input video signal, utilizing a clock signal which is phase synchronized with the input video signal so that change in the state of the input video signal is made easily detectable (Abstract).

Suga teaches a display control apparatus having a plurality of different display modes for displaying a plurality of types of images corresponding to different resolutions and dot clocks (Abstract).

Neither Shibamiya nor Suga, alone or in combination, cures the deficiencies of the combination of Beyers and Knox noted above.

Accordingly, the Applicant respectfully submits that none of the applied art of record, nor any combination thereof, discloses or suggests at least the combination of a control section for controlling accesses in said image memory and said display buffer memory, for reading the first image data group from the first memory area and writing the first image data group in said display buffer memory without intervening with any other memory area or buffer memory, and for reading the second image data group from the second memory area and writing the second image data group in a specified area of said display buffer memory, wherein said control section includes a data expansion control section capable of selectively increasing a data amount of only the second image data group read from said image memory, according to the second

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image data group, and of outputting the data amount to the display buffer memory, as recited in independent claims 1 and 17, as amended.

For at least these reasons, the Applicant submits that independent amended claims 1 and 17 are allowable over the applied art of record. As claims 1 and 17 are allowable, the Applicant submits that claims 2-16 and 19, which depend from allowable claims 1 and 17, respectively, are likewise allowable for at least the reasons set forth above with respect to amended claims 1 and 17.

Conclusion

For all of the above reasons, it is respectfully submitted that the claims now pending patentability distinguish the present invention from the cited references. Accordingly, reconsideration and withdrawal of the outstanding rejections and an issuance of a Notice of Allowance are earnestly solicited.

Should the Examiner determine that any further action is necessary to place this application into better form, the Examiner is invited to contact the undersigned representative at the telephone number listed below.

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In the event this paper is not considered to be timely filed, the Applicants hereby petition for an appropriate extension of time. The Commissioner is hereby authorized to charge any fee deficiency or credit any overpayment associated with this communication to Deposit Account No. 01-2300 referencing client matter number **107317-00030.**

Respectfully submitted,

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